preventing discharge of said parasitic capacitance into the input of said circuit responsive to detection of a negative edge of said input signal.

8. (AMENDED) Apparatus for reducing distortion of a signal applied to an input of a circuit operating at high frequency and having a parasitic capacitance, comprising:

a detection circuit for detecting a change in voltage of said input signal coupled to said input; and

a correction circuit coupled to said detection circuit for compensating for current from said parasitic capacitance to be added to said input signal due to a negative edge of said input signal.

16. (AMENDED) Apparatus for reducing distortion of a signal applied to an input of a circuit operating at high frequency and having a parasitic capacitance, comprising:

a first circuit element for selectively providing current to said parasitic capacitance;

a second circuit element for selectively preventing discharge of said parasitic capacitance into said input; and

a control circuit monitoring said input signal for respectively turning on said first circuit element and turning off said second circuit element when a positive going edge of said input signal is detected and for turning off said first circuit element and turning on said second circuit element when a negative going edge of said input signal is detected;

said first and second circuit elements have a common terminal coupled to said parasitic capacitance;

said first and second circuit elements being transistors.

19. (AMENDED) The method of claim 3 wherein the parasitic capacitance is across said input and ground, the step of preventing discharge including introducing the current to said input.

23. (AMENDED) The method of claim 13 wherein the parasitic capacitance is across said input and ground, the step of preventing discharge including introducing the current to said input.

Cancel claims 7 and 10.

Please add the following new claims 26 and 27.

--26. A method for reducing distortion of a signal applied to an input of a circuit having a parasitic capacitance, comprising the steps of:

detecting a direction of change in voltage of said input signal; and

introducing a current to said parasitic capacitance to compensate for current of said input signal charging said parasitic capacitance responsive to detection of a positive edge of said input signal, thereby eliminating a need for an additional parasitic capacitance to reduce distortion.

27. A method for reducing distortion of a signal applied to an input of a circuit having a parasitic capacitance, comprising the steps of:

detecting a direction of change in voltage of said input signal; and